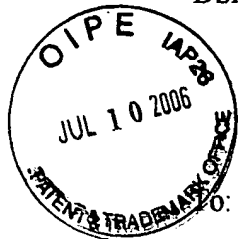


DSR-14452



July 5, 2006

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572  
28 Davis Avenue  
Poughkeepsie, NY 12603

Subject:

Serial No. 10/813,864 03/31/04

Taner Dosluoglu et al.

RED/GREEN PIXEL WITH SIMULTANEOUS  
EXPOSURE AND IMPROVED MTF

#### SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation In An Application.

The following Patents and/or Publications are submitted to comply with the duty  
of disclosure under CFR 1.97-1.99 and 37 CFR 1.56.

#### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States  
Postal Service as first class mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450, on July 5, 2006.

Stephen B. Ackerman, Reg. # 37761

Signature/Date

 7/5/06

U.S. Patent 5,965,875 to Merrill, "Color Separation in an Active Pixel Cell Imaging Array Using a Triple-Well Structure," discloses a digital imager apparatus using the differences in absorption length in silicon of light of different wavelengths for color separation.

U.S. Patent 4,613,895 to Burkey et al., "Color Responsive Imaging Device Employing Wavelength Dependent Semiconductor Optical Absorption," discloses an image sensing element in a solid state imaging device with a plurality of superposed channels disposed at respective distances from a light receiving surface of the device, each of such channels having a different characteristic spectral response due to the differential absorption of light by a semiconductor.

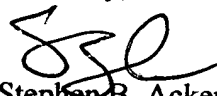
U.S. Patent 4,309,604 to Yoshikawa et al., "Apparatus for Sensing the Wavelength and Intensity of Light," discloses a solid state wavelength detection system responding to output signals derived from a photoelectric semiconductor device.

U.S. Patent 4,011,016 to Layne et al., "Semiconductor Radiation Wavelength Detector," discloses a double-layer photodiode created in an integral structure to form two diodes, the upper diode having a relatively thin active region and the lower diode a relatively thick active region.

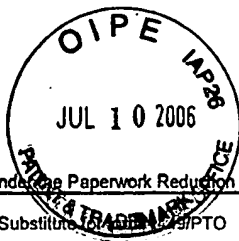
International Patent Application WO 00/62350 to Merrill et al., "Multiple Storage Node Full Color Active Pixel Sensors," discloses arrays of active pixel sensors wherein each of the active pixel sensors is a triple-junction structure to ensure that each pixel sensor in the array measures each of the three primary colors (R-G-B) in the same location.

European Patent Application EP 0 949 689 A2 to Takizawa, "Epitaxial Semiconductor Substrate and Manufacturing Method Thereof; Manufacturing Method of Semiconductor Device and of Solid-State Imaging Device," discusses how the quality of epitaxial semiconductor substrates treated by carbon gettering is evaluated precisely and quickly to use only good-quality ones for manufacturing good-property semiconductor devices, such as solid-state imaging devices.

Sincerely,

A handwritten signature in black ink, appearing to read 'SBA', is written over the printed name.

Stephen B. Ackerman,  
Reg. No. 37761



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PTO/SB/08A (07-05)  
Approved for use through 07/31/2006. OMB 0651-0031  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	10/813,864
		Filing Date	03/31/04
		First Named Inventor	Tamer Dosluoglu et al.
		Art Unit	
		Examiner Name	
Sheet	of	Attorney Docket Number	DSR-14452

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 5,965,875	10/12/99	Merrill	
		US- 4,613,895	09/23/86	Burkey et al.	
		US- 4,309,604	01/05/82	Yashikawa et al.	
		US- 4,011,016	03/08/77	Layne et al.	
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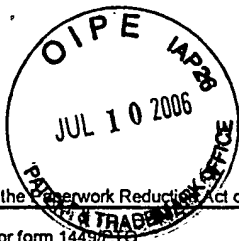
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		EPO 949 689 A2	10/13/99	Takizawa		
		WO 00/62350	10/19/00	Merrill		

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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		Art Unit			
Sheet		of		Examiner Name	
				Attorney Docket Number	DSR-14452

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>

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